



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hiroyuki TOMIMATSU

Group Art Unit: 2814

Application No.: 10/782,810

Examiner: H. Pham

Filed: February 23, 2004

Docket No.: 114371.01

For: SEMICONDUCTOR DEVICE HAVING MULTIPLE SEMICONDUCTOR CHIPS  
STACKED IN LAYERS AND METHOD FOR MANUFACTURING THE SAME,  
CIRCUIT SUBSTRATE AND ELECTRONIC APPARATUS (AS AMENDED)

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the July 26, 2005 Office Action, please consider the following:

**Amendments to the Specification;**

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**